Ceramic Substrate Technical Datasheet

V-MTECK® Cera Substrate Series

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Nanjing. China

V-MTECK® Cera Substrate Series

Features:

The main material of V-MTECK® Series ceramic IC substrate are high-purity aluminum oxide ceramic, Which shows the Excellent thermal stability, good strength, anti-scratch and corrosion performance, which can be used for The high-performance computing, MEMS, CPO IC chip packing etc.

While employ the laser, plasma and Sputtering etc. innovative technology to ensure the IC substrate can Meet with the critical needs in various environment.

Item		Specification and Index	Remark
Tolerance	OUTER L & W	-0.05，+0.2 mm	
	Sub. Thickness	±0.03mm	
	Pitch and Pad	PAD>0.30, ±0.025mm, PITCH>0.50: ±15%	DPC
	Line/Space	Line/Space≥ 25um ±15%	DPC+
	Position	±0.10mm	DPC
Flatness		≤0.5mm	
Micro-Roughness		Ra≤ 2μm，Rz≤ 15μm	
Surface Treatment		Electroless Nickel Immersion Gold	
		Nickel：3～7μm (P:8%±2%)	
		Gold：Ni 3～7μm (P:8%±2%)/Au0.03～0.13 μm	
Working condition		800°C 至-55°C（>300°C，Protective Gas）	
Storage		Vacuum or Nitrogen Gas，15days after Open under dry air ventilation	
Shelf-life		6 months (Vacuum)	

Contact information

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